

MVS Core Technologies Introduction



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Our Purpose

The MVS Core Technologies Project is concerned with a broad spectrum of mainframe-related topics that provide in-depth exploration of IBM's flagship platform in both the hardware and software realms. Hardware areas of interest include z processors, HMC, coupling facilities, STP, and I/O connectivity. Software areas of interest include z/OS operating system components, subsystems and applications such as RSM, VSM, IOS, Parallel Sysplex, JES2, JES3, TSO/E, ISPF, z/OS UNIX, zFS, z/OSMF, and enterprise presentation solutions such as Infoprint. Advanced sessions will discuss architecture, installation, configuration, migration, service, problem determination, and exploitation of the latest and z/OS capabilities. Tools such as ServerPac, HCD/HCM, SMP/E, IPCS, and others are regularly discussed. The Project also offers sessions for those who are new to the platform, intended to help them quickly integrate into their environment and acquire mainframe skills and understanding of the platform, especially in z/OS.

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MVSE Requirements System

- The Requirements system provides a way for SHARE members to discuss, vote and then submit Requests For Enhancements (RFEs) to IBM
- IBM gives SHARE RFE's high priority (as it comes from multiple customers).
- Access via: <https://www.share.org/page/mvse-requirements>. Press the 'Subscribe Now' button.
- The team is made up of several SHARE members as well as two IBM reps: Peter Relson and Brian Keuling
- Come to the after-hours session on Thursday, March 4th, at 4:10pm, where IBM will present their responses to our requests since last SHARE Virtual (joint session with MVSS- the Storage Project). More about the system in my [SHARE'd Intelligence posting](#)